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Time Variant Digital Sigma-Delta Modulator for Fractional-N Frequency Synthesizers

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Abstract—A digital sigma-delta modulator whose noise transfer function changes randomly in time is presented. The spectrum of the output bit-stream is tone free for any constant input value. The time-variant behaviour causes a white floor in the noise output spectrum, but the effect is negligible if the levels is kept well below other white noise sources. The scheme preserves the high frequency phase noise even at critical inputs. The hardware cost is affordable and the system complexity is lower than using dither.

I. INTRODUCTION

Fractional-N synthesizers are important blocks widely used in modern communication systems. The frequency synthesizer is required to ensure low phase noise, good loop speed, limited spur levels, along with fine-grained resolution (channel spacing) at any fractional value, [1-2].

In synthesizers, the Voltage-Controlled Oscillator (VCO) frequency is divided by a number that varies around a fixed integer value \( N \) by a variable amount \( \Delta N(t) \). The parameter \( \Delta N(t) \) is the result of the conversion of the fractional part given by a digital sigma-delta (\( \Sigma \Delta \)). The aim of the \( \Sigma \Delta \) is to avoid repetitive patterns in the time domain because they give spurious tones, [3]. The designer obtains the result with high order schemes because many integrators and multiple feedback loops suitably scramble the data. However, for critical values of the fractional input even high order architectures show tones in the output spectrum that are removed by dithering, [4].

Unfortunately, non-linearities reduce the effectiveness of dithering. Often, even with a dither that completely removes tones and obtains a well shaped noise spectrum, the phase noise is not good because of the non-linearity of various blocks, like the dead-zone of the charge pump. The non-linearity mixes the tones not removed by the loop filter and worsen the phase noise contributed by the \( \Sigma \Delta \) in frequency regions where the other noise terms are negligible.

This paper proposes an effective method for destroying the repetitive patterns of time-domain \( \Sigma \Delta \) signals. Instead of using a noise-like term that is added to signals (dither), a noise-like additive term for the modulator coefficients is used. Simulations at the behavioural level show that dithering in multiplication instead of in addition is more effective. The phase noise performance remains remarkable even with non-linearities in the synthesizer loop.

II. CONVENTIONAL SOLUTIONS

The basic block diagram of a conventional fractional-N synthesizer is shown in Fig. 1. The Phase Frequency Detector (PFD) receives the reference clock and the VCO divided by \( N + \Delta N(t) \). The loop is then closed by the charge pump and loop filter. The main designer concerns are to ensure a low phase noise and avoid spurs in the output signal. The noise sources affecting the blocks of Fig. 1 are the \( \Sigma \Delta \) noise, the PFD, the charge pump, the reference clock and the VCO noise. Since these noise sources are not correlated, they contribute quadratically to the overall phase noise

\[
P_{N}^2 = P_{N\Delta}^2 + P_{N_{FD}}^2 + P_{N_{CP}}^2 + P_{N_{Clock}}^2 + \ldots
\]

(1)

The power spectra of every single component, assuming a linear response, are summed up.

The undesired in-band spurious tones are typically caused by the \( \Sigma \Delta \) because, with critical fractional values input, it gives rise to limit cycles in the time domain, [5-6].

Since the digital \( \Sigma \Delta \) is required to produce low noise at low frequencies and no spurs, the order of the modulator must be at least 3 with the spur possibly cancelled by dithering. High order modulators are the right choice because they are able to obtain the conditions for which the quantization error is equivalent to noise. However, hidden tones in the high frequency region of the spectrum create problems.

Notice that having a strongly shaped \( \Sigma \Delta \) noise is not relevant because at low frequencies other sources of noise are

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Fig. 1. Fractional-N frequency synthesizer block diagram.
dominant. Indeed, it is necessary, as outlined by equation (1), that the $\Sigma\Delta$ contribution is lower than other noise sources by at least 10-20 dB. However, high order modulators are necessary because, with low order, the dither is not capable to spoil the repetitive patterns.

The design of single loop high-order modulators is difficult because of stability conditions. For this reason, the designer prefers using MASH architectures that obtain high order noise shaping by cascading first order modulators. Since the noise transfer function (NTF) of digital implementation is exactly what expected, the used architectures, made by common digital blocks such as accumulators, adders, and D flip flops, are convenient and effective. However, the output of MASH schemes is multi-bit. For a MASH-11 $\Delta N$ ranges from -1 to +2, while for a MASH-111 $\Delta N$ varies from -3 to +4. Therefore, the digital divider is complex and the power of the quantization error is larger.

Even with a MASH-111, the $\Sigma\Delta$ output spectrum is not completely tone-free. Fig. 2 shows a typical situation of an undithered output spectrum. To cancel out the tones, it is necessary to use dithering. It is the injection of small pseudo-random signals at the three inputs of the first order modulators. The pseudo-random sequence at the first integrator is white, the ones at the second and third are shaped by $(1 - z^{-1})$ and $(1 - z^{-1})^2$, respectively, to limit the in-band effect. The resulting spectrum looks like the second curve of Fig. 2.

This and other techniques have been used with good results. However, tones are not transformed into noise, but their amplitude is just reduced. Possible non-linearities of blocks used in the feedback loop cause mixing of tones that show up in the low frequency band as white floor. In some unfortunate case, the mixing generates a large component that is well visible in the output spectrum. The effect is outlined in Fig. 3 that shows the spectrum at the output of a MASH-111 and its processing with a non-linear block

$$Y(t) = X(t) \left[1 + \alpha X(t) + \beta X(t)^2\right]. \quad (2)$$

The good spectrum at the output of the MASH-111 plus dithering at the three inputs degrades significantly with relatively small second order and third order distortion terms ($\alpha = 2^{-15}, \beta = 2^{-15}$).

The obtained result leads to the following observations: high-order modulators are not fully effective in removing tones as the high-pass noise shaping of high-order modulators is corrupted by non linearity that cause an equivalent white noise.

III. PROPOSED METHOD

The dithering method foresees additive pseudo-random terms in critical points of the modulator. The method proposed here is to use multiplicative pseudo-random dithering that changes the coefficients used in the modulator architecture. As shown in Fig. 4 (a), the first order modulator adds the quantization error to the input. The proposed method foresees the change depicted in Fig. 4 (b). The quantization error is multiplied by a coefficient that changes randomly in time as given by

$$\begin{align*}
Y(t) &= X(t) [1 + \alpha X(t) + \beta X(t)^2].
\end{align*}$$

![Fig. 4. Internal node of a conventional (a) and of the proposed (b) modulator.](image-url)
\[ K(nT) = 1 + \epsilon_K(nT) \]  
\[ (3) \]

where \( \epsilon_K(nT) \) is a small convenient random number with constant module and random sign. The result is a sigma-delta with discrete-time variant coefficients. For digital sigma-delta modulators, the word-length of the added term must be quantized at the resolution of the accumulator. The operation just requires to truncate the dithered term or, eventually, by passing the truncated part through a first order digital \( \Sigma \Delta \). Notice that the implementation of the time-variant architecture requires some additional hardware, as it is for the conventional dithering techniques. Fig. 5 (a) and (b) shows the implementations of the schemes of Fig. 4 (a) and (b), respectively.

Notice that the study of time variant \( \Sigma \Delta \) modulators can not be done with the linearized method used for conventional \( \Sigma \Delta \) architectures, but needs a special analysis similar to the one developed for time-variant filters. That study is not done in this work. The effectiveness of the method is proved by just simulations in the time-domain.

The scheme of Fig. 4 (b) can be the basis for the design of high order \( \Sigma \Delta \) as, for example, the MASH-11 or the MASH-111. Alternatively, it is possible to obtain second order or third order noise shaping with the schemes of Fig. 6. The inverse of the quantization error needs the following processing

\[ H_2 = (2 - z^{-1}); \quad H_3 = (3 - 3z^{-1} + z^{-2}) \]  
\[ (4) \]

that identifies coefficients that can be possibly transformed into time variant by using dithering multipliers. The various multipliers used can have different amplitude and uncorrelated random signs.

Fig. 7 shows the output spectra of a time-variant second order and third order modulator with a critical value of constant input, used \( \epsilon_K = 1/2^{12} \) and with uncorrelated random signs. Our single-bit pseudo-random generator is made by a chain of 11 flip-flop with a suitable feedback logic that obtains a sequence \( 2^{11} \) long. The scheme is shown in Fig. 8. Uncorrelated sequences are given by using different taps of the inverters chain. The spectra of Fig.7 show that the using time variant coefficients causes a noise floor and reduces the noise amplification at high frequencies. The first effect is inessential, if the floor level is low. The second feature is beneficial for reducing the high frequency noise. Moreover, the obtained spectra are much more noise-like than the dithered counterparts.

A convenient test for verifying the method effectiveness is to pass the multi-bit output through a non-linear block with
response given by equation (2). The result is that the same coefficients ($\alpha = 2^{-15}$, $\beta = 2^{-15}$) used in the simulation of Fig. 3 gives no noticeable changes in the time-variant MASH-111, as shown in Fig. 9. To obtain noise floor above -140 dBFS, it is necessary to use $\alpha = 2^{-13}$, $\beta = 2^{-13}$, a two orders of magnitude higher distortion. Fig. 9 shows that the noise floor of the conventional dithered MASH-111 is obtained with $\alpha$ and $\beta$ as large as $2^{-11}$.

**IV. TIME-VARIANT $\Sigma\Delta$ IN FRACTIONAL-N SYNTHESIZER**

The effective operation of the proposed $\Sigma\Delta$ architecture has been verified with behavioural simulation by its use in a fractional-N frequency synthesizer, [5-6]. The reference frequency is 25 MHz and the fractional division number is 71.001953. The output frequency is 1.77504 GHz. Non-linearity has been included in the loop. In particular, the charge pump has a dead zone of 4.5%. The conventional dithered MASH-111 works as expected with linear components around the loop, but its performance worsens with the non-linearities. On the contrary, the time variant scheme does not experience any limit with the used non-linearities. Fig. 10 compares the two phase-noise plots. The phase noise in the ideal case is also shown.

The method is also effective with a MASH-II modulator for which the required $\Delta N(t)$ is half the one of the MASH-III. Fig. 11 shows that the simulated phase noise is clean with an expected higher level (about 10 dB) because of a lower order in the noise shaping.

The obtained results minimally depend on the value of the fractional input. Therefore, depending on the system specifications, the designer can use affordable second or third order scheme with tone-free performance.

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