A Current-Mode CMOS Integrated Microsystem for Current Spinning Magnetic Hall Sensors

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Abstract—A magnetic Hall sensor working in the current-mode is presented. The proposed sensing device is composed by two Hall plates able to provide a differential current at the output nodes. The sensor, fabricated in a standard 0.18-µm CMOS technology, uses the spinning-current technique to compensate for the offset and obtains a sensitivity \( I_{\text{Hall}}/(B_{\perp} I_{\text{bias}}) \) better than 0.02 T\(^{-1}\) for magnetic fields ranging from 0 to 10 mT.

I. INTRODUCTION

A magnetic Hall sensor is a transducer, which converts a magnetic field into a corresponding electrical signal. Magnetic Hall micro-sensors generally use integrated Hall plates as sensing devices, which are fully compatible with standard CMOS technologies. Hall effect sensors are widely used for proximity switching, positioning, speed detection, and current sensing applications.

Typically, Hall plates are used in voltage-mode and for many years voltage-mode Hall sensors have been absolutely dominant in most of the applications. This means that the magnetic field to be measured is converted into an output voltage. Signal-to-noise ratio (SNR) and offset are important features in Hall sensors performance evaluation. Several techniques have been developed in order to improve these characteristics. Most of them, [1]–[5], are based on conventional voltage-mode Hall transducers. Parasitic capacitances and their effect, specially at high frequency operation, affect performance of voltage-mode solutions.

As an alternative solution, an Hall sensor can work in the current mode. The output is current and not voltage. In the current-mode Hall sensors there is no variation of the terminal potentials and so no influence of the parasitic capacitances. Also, it can be possible to use of smaller number of terminals, therefore the ultimate miniaturization of the device is easier, [1].

This paper presents a low power current-mode Hall sensor microsystem with differential output current, as shown in Fig. 1. The physical structure of the proposed current-mode Hall sensor is the same of modern devices, with the same possibility of compensating for the offset caused by mismatch (current spinning). The difference is in driving and extracting the signal. Extensive and accurate physical simulations and behavioral models of the current-mode Hall sensors allowed optimizing its size, shape and performance for the chosen technology, a standard 0.18-µm CMOS technology. The offset at the output terminals of the sensors is eliminated by spinning current technique and measurement results show good sensitivity better than 0.02 T\(^{-1}\) for magnetic fields in the range from 0 to 10 mT. The circuit operates properly with a bias current as low as 12 µA. The benefit of having current at output is that its integration over a defined time period determines an output voltage with a gain proportional to the integration time.

The paper is organized as follows. In Section II the current-mode technique is presented, discussed and compared with the voltage-mode counterpart. Section III describes the microsystem implementation, while measurement results are presented in section IV. Section V draws some conclusions.

II. CURRENT-MODE HALL SENSORS

The description of an Hall sensor working in the voltage-mode can be found in [1] and [3]. When operating in this mode, the Hall voltage \( V_{\text{Hall}} \) is proportional to the sensor bias current, \( I_{\text{bias}} \), the current-related voltage-mode sensitivity, \( S_V \), and the applied perpendicular external magnetic field, \( B_{\perp} \),

\[
V_{\text{Hall}} = S_V I_{\text{bias}} B_{\perp} \quad (1)
\]

The current-mode Hall sensor principle has been described in [6]. In this microsystem, the method is applied to a couple of Hall plates in order to achieve differential currents at the output.

Fig. 2 shows the proposed current-mode Hall sensor. The basic idea consists in injecting a current, \( I_{\text{b1}} \), laterally in two consecutive arms of the first cross-shaped Hall plate and draining nominally the same current, \( I_{\text{b2}} \), from two consecutive...
arms of the second Hall plate. In this condition, with zero magnetic field the sensor structure is balanced thus making null the output currents, \(I_{HN}\) and \(I_{HP}\). An external magnetic field unbalances the sensor, providing two Hall currents, \(I_{Hall}\), at the output nodes. The Hall current, for a cross-shaped Hall plate, can be expressed as [6]

\[
I_{Hall} = \frac{\beta B L_{bias}^2}{1 - (\beta B L_{bias})^2}
\]  

(2)

where \(\beta\) is the magnetic resistance coefficient in presence of a magnetic field. It is defined as

\[
\beta = \frac{R(B) - R(B=0)}{R(B=0) B_L}
\]

(3)

where \(R(B=0)\) and \(R(B)\) define the Hall plate resistance in absence and presence of an external magnetic field, respectively.

The output currents of each Hall plate, \(I_{H+}\) and \(I_{H-}\), in Fig. 2, are

\[
I_{H+} = \frac{I_{bias}}{2} + \frac{I_{Hall}}{2}
\]

(4)

\[
I_{H-} = \frac{I_{bias}}{2} - \frac{I_{Hall}}{2}
\]

(5)

In the proposed configuration, the use of a couple of Hall plates makes the sensor output currents, \(I_{HP}\) and \(I_{HN}\), being the difference of the output currents of each Hall plate. Using equations (4) and (5), the sensor output currents are given by

\[
I_{HP} = I_{H+} - I_{H-} = I_{Hall}
\]

(6)

\[
I_{HN} = I_{H-} - I_{H+} = -I_{Hall}
\]

(7)

The current-mode magnetic sensitivity is used to evaluate the performance of current-mode Hall sensors. It is defined as

\[
S_I = \frac{I_{Hall}}{I_{bias} B_L}
\]

(8)

For the used configuration, the difference of the output currents is \(2 \times I_{Hall}\), thus doubling the sensor sensitivity with respect to the conventional implementation.

### III. Circuit Implementation

The proposed microsystem, shown in Fig. 1, consists of two magnetic Hall plates used as the sensing device, a number of switches to implement the current spinning and a biasing circuit. This Section describes the implementation details of each block.

#### A. Current-Mode Hall Plates

Several current-mode Hall structures have been modeled and evaluated with respect to noise, offset and sensitivity using COMSOL Multiphysics\textsuperscript{TM} and Verilog-A, [6]. The cross-shaped model is the optimum plate structure to fit the lowest noise and residual offset and the best sensitivity. In addition, the symmetrical cross-shaped Hall plate grants high sensitivity and immunity to alignment tolerances resulting from the fabrication process.

A low doped N-well makes the Hall plates. In order to reduce the flicker noise and surface carrier losses, a shallow highly doped P\textsuperscript{+} conductive top layer covers the surface of the active area. The four contact regions in the N-well diffusion are N\textsuperscript{+} highly doped.

Fig. 3 shows the model geometry of the two Hall plates used in a COMSOL Multiphysics\textsuperscript{TM} simulation: the maximum width and length of each plate is 8 \(\mu\)m. The figure shows the simulated surface electrical distribution and the current distribution when a perpendicular magnetic field of 10 mT is applied. The simulation, carried out with physical parameters fitting the used CMOS technology, uses the nominal bias current of 12 \(\mu\)A injected in terminals 1 and 2 and drained from terminals 3 and 4.

#### B. Current Spinning

Since the output offset strongly limits the DC resolution of the sensors, the use of the cross-shaped Hall plate enables the current spinning technique, [7] [8], to reduce offset and 1/f noise.

The output and supply terminals of each Hall plate are periodically interchanged so that the input bias current polarity is rotated in each state whereas the offset appears at the output terminals. The plates are clocked with four phases. At the
Fig. 4. Hall plates with switches configuration for current spinning.

Fig. 5. Phases timing diagram for current spinning operation.

end of a complete spinning operation, the average of offset becomes zero.

Fig. 4 shows the sensor and switches configuration driven by the four phases whose time diagram is given in Fig. 5. During each phase, $\Phi_1$, $\Phi_2$, $\Phi_3$, and $\Phi_4$, for each plate two terminals are connected to $I_{bias}$ while the remaining two are the outputs. The implementation uses 32 switches, each of them is a complementary CMOS pair.

C. Biasing Circuit

The sensor structure of Fig. 2 uses two current generators nominally equal. A possible mismatch brings the one with the larger value to the triode region. This imposes to use a feedback control to make equal the two current generators. Fig. 6 shows the used circuit. The P-channel cascade current mirror replicates the bias current, $I_{bias}$. A second cascade mirror gives rise to the sink current. Its value is systematically less than the required voltage to leave some room for the common mode control. This current is a P-channel differential pair sensitive to the common mode input. The current flowing in $M_{12}$ mirrored by $M_{15} \sim M_{18}$ makes the common mode voltage at output of the sensor equal to VCM, thus ensuring that the drain and sink bias currents are equal.

IV. Measurement Results

A sensor prototype has been fabricated in a standard 0.18-µm CMOS process with 6 metal and 2 poly layers. Its microphotograph with its layout back-annotated is shown in Fig. 7. The figure highlights main circuital blocks. The microsystem occupies an active area of $80 \times 50 \, \mu m^2$. The nominal supply voltage and the nominal bias current are 1.8 V and 12 µA, respectively. The current spinning frequency is 1 MHz.

A measurement setup is shown in Fig. 8. A digital signal generator/analyser provides the four phases for spinning current. Helmholtz coils apply to the integrated microsystem a magnetic field ranging from 0 to 10 mT.

Fig. 9 plots the measured differential sensor output current, $I_{Hall}$, as a function of the sensor bias current for two different
magnetic field, 5 mT and 7 mT. \( I_{bias} \) is ranging from 0 to 48 \( \mu \)A. The figure shows that the sensor has a good linearity with respect to the applied magnetic field.

Fig. 10 gives the sensitivity, \( S_f \), as a function of the sensor bias current (ranging from 6 to 48 \( \mu \)A) for the two above external magnetic fields. The measured sensor sensitivity is within the 0.013 \( T^{-1} \)–0.02 \( T^{-1} \) range.

Table I summarizes the magnetic and electrical microsystem performance.

<table>
<thead>
<tr>
<th>TABLE I. PERFORMANCE SUMMARY</th>
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<tbody>
<tr>
<td>Technology</td>
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<tr>
<td>Sensor Area</td>
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<tr>
<td>Plate Area</td>
</tr>
<tr>
<td>Number of Sensors</td>
</tr>
<tr>
<td>Current Spinning Frequency</td>
</tr>
<tr>
<td>Supply Voltage</td>
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<tr>
<td>Sensitivity</td>
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<tr>
<td>Measurement Range</td>
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<td>Power Consumption</td>
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Fig. 10. Measured sensor sensitivity as a function of the biasing current at different magnetic fields.

V. CONCLUSION

This paper presented a magnetic current-mode Hall microsystem. Two Hall plates operating in the current-mode and able to provide differential currents at the output nodes has been fabricated in a standard 0.18-\( \mu \)m CMOS process. Measurement results show that the Hall sensor achieves a sensitivity better than 0.02 \( T^{-1} \) when the magnetic field is in the range from 0 to 10 mT. The use of the crossed-shaped Hall plates and a current-mode approach enables current spinning technique for offset cancellation. The power consumption is in the tens of \( \mu \)W range. Moreover, this Hall device can be easily integrated with a readout circuit in a single chip to increase the magnetic sensitivity.

REFERENCES